

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Takashi Nakano, et al. **Examiner:** R. Potter
Serial No.: 10/518,157 **Art Unit:** 2822
Filed: December 10, 2004 **Docket:** 18497
For: SEMICONDUCTOR DEVICE,
SEMICONDUCTOR CIRCUIT AND
METHOD OF FABRICATING
SEMICONDUCTOR DEVICE **Dated:** April 12, 2007

Confirmation No.: 3521

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. §1.111

Sir:

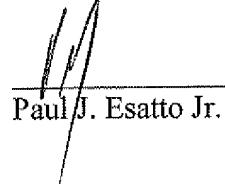
Applicants submit this Amendment for entry thereof in response to the Office

Action mailed January 12, 2007.

CERTIFICATION OF ELECTRONIC TRANSMISSION

I hereby certify that this paper is being electronically transmitted to the Patent and Trademark Office on the date shown below.

Dated: April 12, 2007



Paul J. Esatto Jr.